Attorney Docket: 951/49164
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: MARTIN PELLER

Serial No.: NOT YET ASSIGNED   Group Art Unit: NOT YET ASSIGNED

Filed: September 11, 2000   Examiner: NOT YET ASSIGNED

Title: DATA BUS FOR A PLURALITY OF NODES

PRELIMINARY AMENDMENT

Box PCT Application
Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination please amend the above-identified application as follows:

IN THE SPECIFICATION:

A substitute specification is submitted herewith.

IN THE CLAIMS:

Please cancel 1 and 2 and add new claims 3-6 as follows:

-- 3. A data bus arrangement for connecting a plurality of nodes to one another, said arrangement comprising:

a logic decision gate having a plurality of inputs for receiving a corresponding plurality of first electrical signals routed from said plurality of nodes wherein an output of the logical decision gate is connected in parallel to provide second electrical output signals routed to each of said plurality of nodes;